TASK CONCURRENCY MANAGEMENT DESIGN METHOD Catthoor et al.

Appl. No.: 09/935,789 Atty Docket: IMEC218.001AUS



Hardware

Software design

design

Hardware/software co-design

Task—level | specification

operations (white box)

grouping of ALL

Task definition =

Specification



3

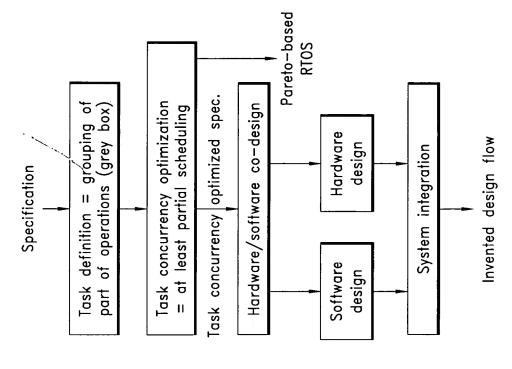


FIG. 1A (PRIOR ART)

Prior-art design flow

System integration

FIG. 1B

